

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

SELF-CONTAINED PROCESSOR SUBSYSTEM AS
COMPONENT FOR SYSTEM-ON-CHIP DESIGN

Application Number :

Confirmation Number:

First Named Applicant: Christos Georgiou

Attorney Docket Number: FIS920030113

Art Unit:

Examiner:

Search string: (6032245 or 4952930 or 4929939 or 5189314 or 6215412 or 4633394 or 4829511 or 5798918 or 6331977 or 6262594 or 6038630 or 20030067930 or 20030067913 or 20030046492 or 20020184419 or 20020176402 or 20020172197 or 20010047465 or 20010042147).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
ICSL	1	6032245	2000-02-29	Georgiou et al.		712	23
ICSL	2	4952930	1990-08-28	Franaszek, et al.		340	2, 23
ICSL	3	4929939	1990-05-29	Varma, et al.		340	2, 24
ICSL	4	5189314	1993-02-23	Georgiou, et al.		327	114
ICSL	5	6215412	2001-04-10	Franaszek, et al.		340	825, 02
ICSL	6	4633394	1986-12-30	Georgiou, et al.		710	240
ICSL	7	4829511	1989-05-09	Georgiou.		398	55
ICSL	8	5798918	1998-08-25	Georgiou, et al.		700	28
ICSL	9	6331977	2001-12-18	Spaderna, et al.		370	360
ICSL	10	6262594	2001-07-17	Cheung, et al.		326	38
ICSL	11	6038630	2000-03-14	Foster, et al.		710	317

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
ICSL	1	20030067930	2003-04-10	Salapura et al.		370	412
ICSL	2	20030067913	2003-04-10	Georgiou, et al.		370	389
ICSL	3	20030046492	2003-03-06	Gschwind, et al.		711	118

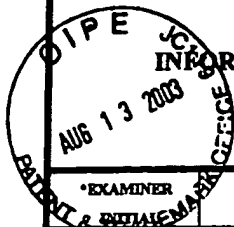
Ken S. Kim

2-9-06

103K	4	20020184419	2003-12-05	Creedon, et al.		710	100
	5	20020176402	2002-11-28	Karim		370	352
	6	20020172197	2002-11-21	Dale, et al.		370	386
	7	20010047465	2001-11-29	Liu		712	43
103V	8	20010042147	2001-11-15	Adams, et al.		710	100

Signature

Examiner Name	Date
Ken S. Kim	2-9-06



INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)

FIS920030113US1 (16497)

Application Number

10/604,491

Applicant(s)

Christos J. Georgiou, et al.

Filing Date

07/25/03

Group Art Unit

Unassigned

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

109K Ryu, et al., "A Comparison of Five Different Multi processor SoC Bus Architectures", Georgia Institute of Technology Electrical and Computer Engineering, Atlanta, GA

1091C Brinkman, et al., "On-Chip Interconnects for Next Generation System-on-Chips", Heinz Nixdorf Institute and Department of Electrical Engineering, University of Paderborn, Germany, Fraunhofer Institute of Microelectronic Circuits and Systems, Duisburg, Germany

105K Berry, "SYSTEM-ON-CHIP MARKETS AND TRENDS", Electronic Trend Publications, Inc. San Jose, CA, Second Edition, 2003, pp. 1-1 -5-2

EXAMINER

Ken S. Klein

DATE CONSIDERED

2-9-06

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.